



RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 1765

#27
ca
9/30/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki Enomoto
Serial No.: 09/019,087
Filed: 02/05/1998
For: MANUFACTURING METHOD OF
SEMICONDUCTOR IC DEVICE

Docket No.: TIJ-24816
Art Unit: 1765
Examiner: Perez Ramos, V
Confirm. No.: 1640

RECEIVED

SEP 30 2002

REQUEST FOR RECONSIDERATION UNDER U.S.C. § 116

TC 1700

Assistant Commissioner For Patents
Box AF
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that on this date, the above
correspondence is being deposited with the US Postal
Service as First Class Mail in an envelope addressed to:
Assistant Commissioner for Patents, Box AF, Washington,
DC, 20231.

William B. Kempler, Reg. No. 28,228

Date

The following remarks are offered in response to the Examiner's Office Action dated 07/23/2002. They are respectfully submitted as a full and complete response to that Action.